

Notice of Allowability	Application No.	Applicant(s)	
	10/552,351	PARK ET AL.	
	Examiner Elias Ullah	Art Unit 2812	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to 8/30/2007.
2. The allowed claim(s) is/are 1-7.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All
 - b) Some*
 - c) None
 of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date 8/30/2007
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

DETAILED ACTION

This office action is in response to applicant remarks filed on 8/30/2007.

Allowable Subject Matter

Claims 1-7 are allowed.

REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance: with respect to claim 1, there is no prior art available or obvious motivation to combine elements of prior art which teach method of fabricating a semiconductor probe which includes a cantilever and a tip doped with first impurities formed on an end portion of the cantilever, wherein a resistive region lightly doped with second impurities is formed at a peak of the tip, and first and second semiconductor electrode regions heavily doped with the second impurities are formed at inclined surfaces of the tip, the polarity of the second impurities being opposite to that of the first impurities, the method comprising: forming a stripe-shape mask layer on a substrate doped with first impurities and forming first and second semiconductor electrode regions by heavily doping second impurities on the substrate uncovered by the mask layer, the second impurities being opposite in polarity to the first impurities; annealing the substrate to narrow a distance between the first and second semiconductor electrode regions and form resistive regions lightly doped with the second impurities at outer boundaries of the first and second semiconductor electrode regions; patterning the mask layer in a predetermined shape and etching a portion of a top surface of the substrate not covered by the patterned mask layer to form a resistive tip; and etching a bottom surface of the substrate to form

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a cantilever with the resistive tip formed at an end portion thereof. Claims 2-7 are dependent on claim 1.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Elias Ullah whose telephone number is (571) 272-1415. The examiner can normally be reached on weekdays, between 8AM-5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, MICHAEL LEBENTRITT can be reached on (571) 272-1873. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Elias Ullah
September 17, 2007

SCOTT B. GEYER
PRIMARY EXAMINER

MBG 9/17/07